

FIG.1A

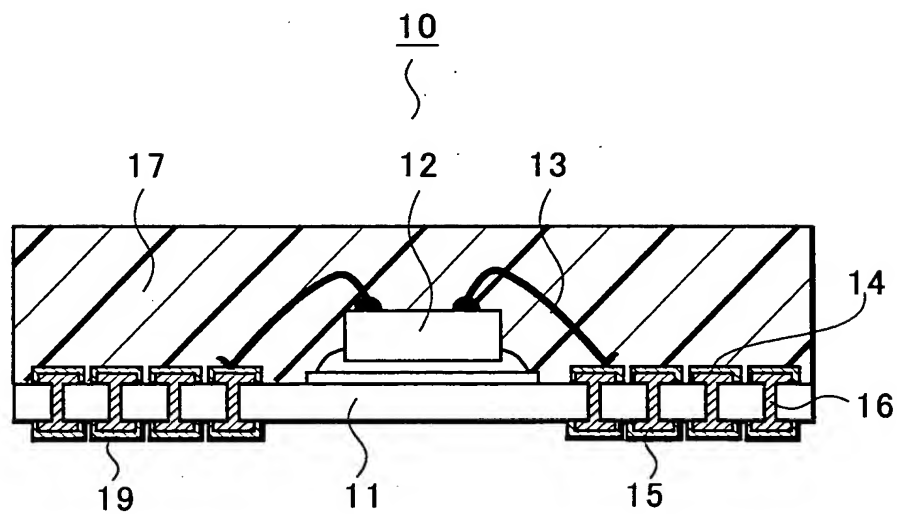


FIG.1B

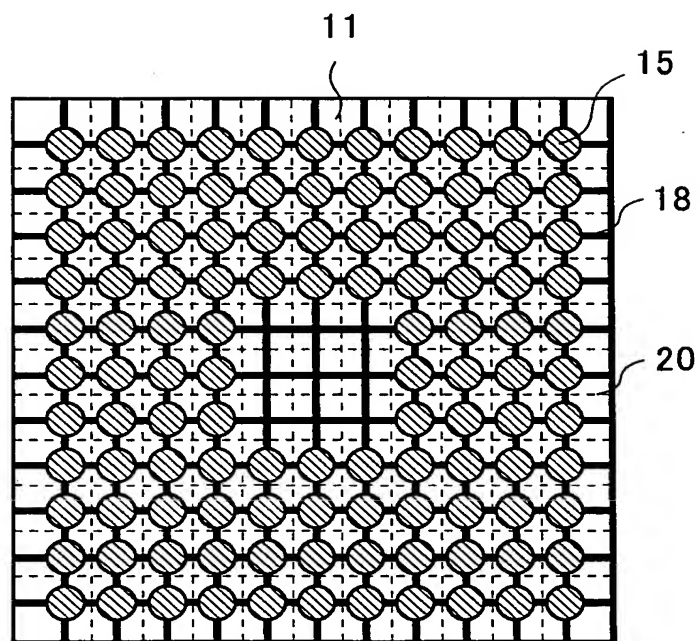


FIG.2A

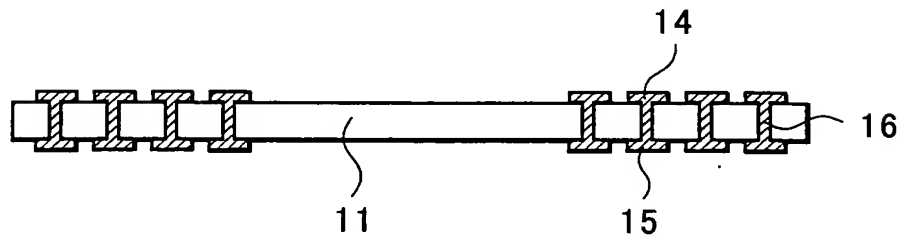
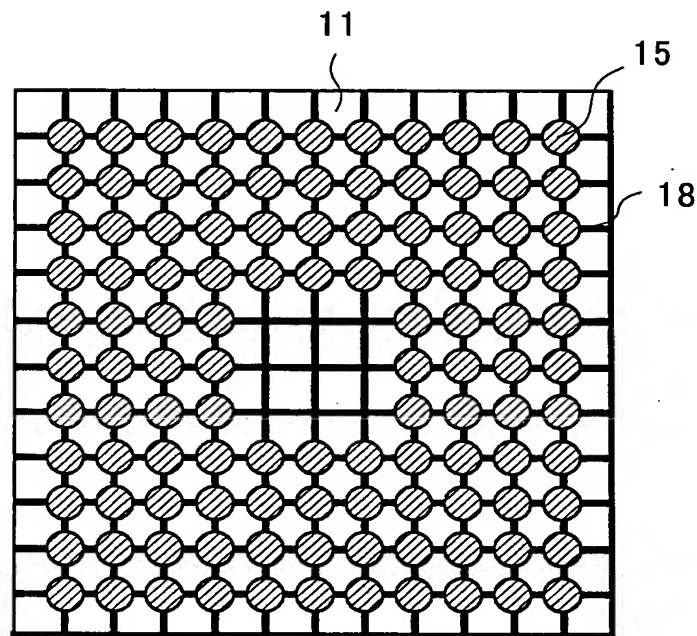


FIG.2B



Applicant(s): Kiyoshi Mita

METHOD FOR MANUFACTURING MOUNTING SUBSTRATE  
AND METHOD FOR MANUFACTURING CIRCUIT DEVICE

FIG.3

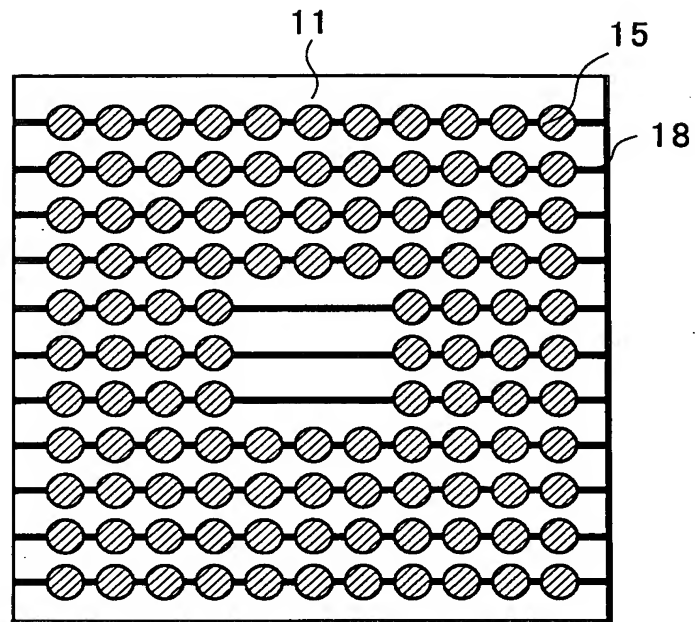


FIG.4A

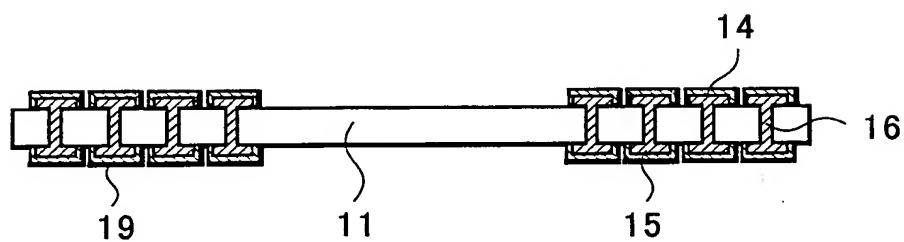


FIG.4B

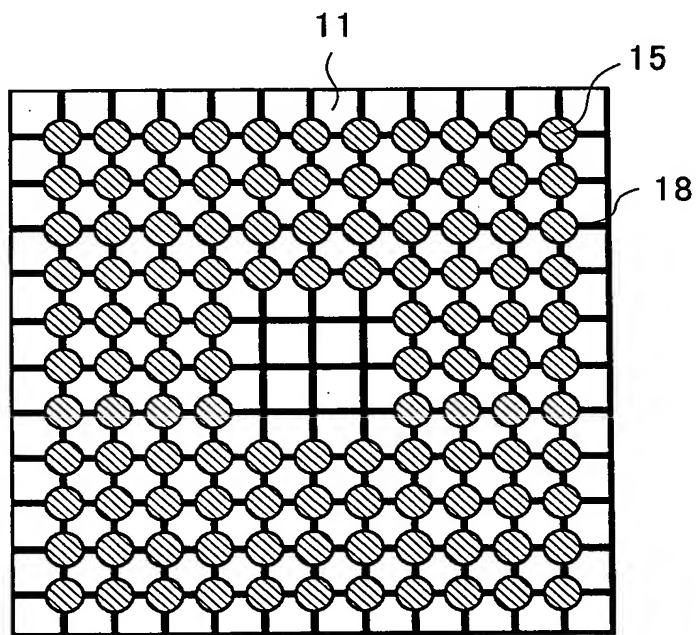


FIG.5

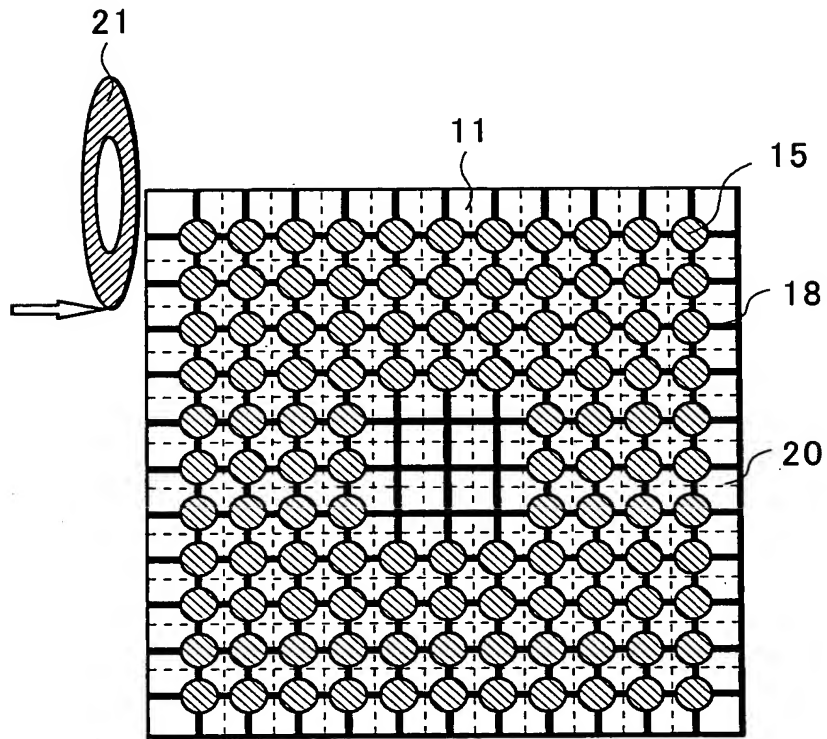


FIG.6

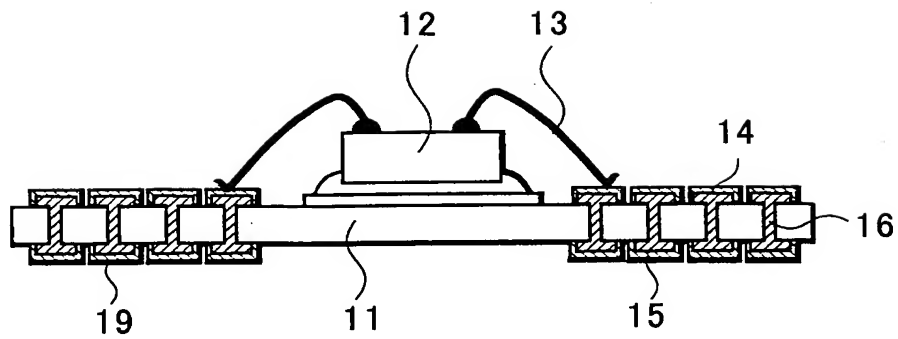


FIG. 7A

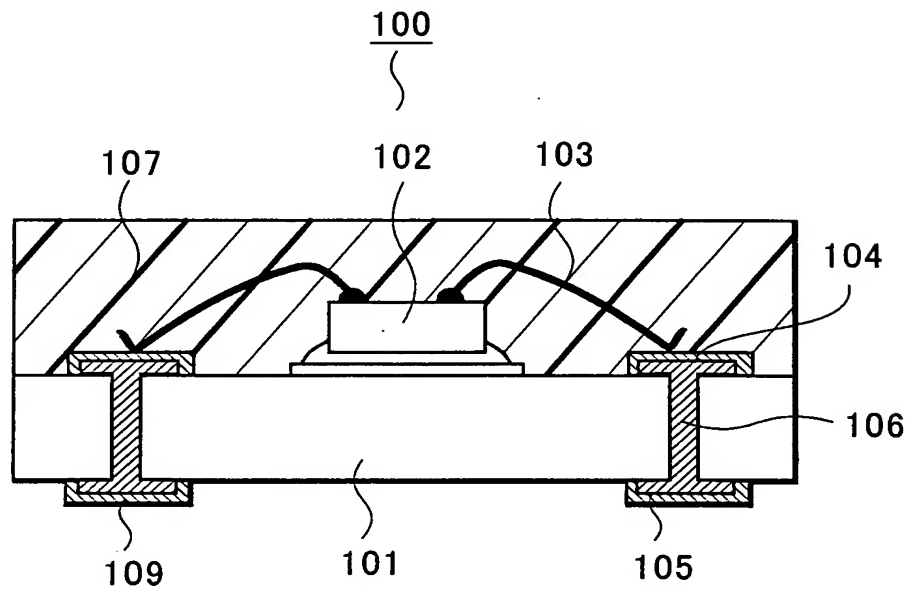


FIG. 7B

